

	L #	Hit	S arch T xt	DB
1	L1	6731	(257/690,691,693,698,700,723,724,730,747,778,787).ccls.	USP AT; US-P GPU B
2	L2	4927	(361/704,707,719,720,760,772,777,783).ccls.	USP AT; US-P GPU B
3	L3	3693	(174/250,253,260,261,262).ccls.	USP AT; US-P GPU B
4	L4	1362 7	1 2 3	USP AT; US-P GPU B
5	L5	2321	4 and @pd>=20020901	USP AT; US-P GPU B
6	L6	1266	5 and (bump\$1 ball\$1)	USP AT; US-P GPU B
7	L7	1108	6 and solder\$3	USP AT; US-P GPU B

	L #	Hit	S arch T xt	DB
8	L8	396	6 and ((thermal with expansion) tce cte tec)	USP AT; US-P GPU B
9	L9	2320	5 and @ad>=19950430	USP AT; US-P GPU B
10	L10	1	5 and @ad<=19950430	USP AT; US-P GPU B
11	L12	9	11 and @ad<=19980430	USP AT; US-P GPU B
12	L11	870	6 not 8	USP AT; US-P GPU B
13	L13	1724	(257/737,738).ccls.	USP AT; US-P GPU B
14	L14	1483	13 not 6	USP AT; US-P GPU B

	L #	Hit	S arch T xt	DB
15	L16	844	15 and solder\$3	USP AT; US-P GPU B
16	L15	901	14 and thermal	USP AT; US-P GPU B
17	L17	8	("4764804" "5093986" "5216278" "5293072" "5397917" "5468655" "5473512" "5512786").PN.	USP AT
18	L18	1034	257/786.ccls.	USP AT; US-P GPU B
19	L19	543	18 and (ball\$1 bump\$1)	USP AT; US-P GPU B
20	L20	470	19 not 15	USP AT; US-P GPU B
21	L21	2	("5168368" "5468999").PN.	USP AT
22	L22	1451	(228/180.21,180.22).ccl s.	USP AT; US-P GPU B

	L #	Hit	S arch T xt	DB
23	L23	119	22 and @pd>=20020901	USP AT; US-P GPU B
24	L24	111	23 not (15 20)	USP AT; US-P GPU B